EXTERNAL SCHOLARS

ONGOING EXTERNAL SCHOLARS

S.No	Candidate Name & Designation	Presently working Institution/ Industry	Provisional Registration Details		Title of the Research Work	Present status of the PhD (Doing coursework/ Confirmation Received / Synopsis submitted/ Thesis submitted)
			Year of Reg.	Reg.No.		
1.	Mr. Silambarasan D Assistant Professor / ECE	Anna University Regional Centre, Coimbatore	2015	1513469430	High speed residue system for cryptography applications	Confirmation Received
2.	Mr. Maragatharaj S Assistant Professor / ECE	Knowledge Institute of Technology, Kakapalayam Chennai	2016	1613469289	Design of massive input multi output low power variable digital filter banks for ultra wide band spectrum sensing in cognitive radio systems.	Confirmation Received

3.	Mr.Nelson Kingsley Joel.P Assistant Professor / ECE	Sri Ranganathan Engineering College, Coimbatore.	2015	1514469960	Implementation of Efficient Algorithms for Removal of Noises in Digital Images	Confirmat-ion Received
4.	Mr.Lenin Raja R Assistant Professor / ECE	Sri Vidhya College of Engg. & Tech., Virudhu -nagar.	2015	1514469152	An Enhanced switching technique for reducing power consumption using multi dynamic node	Confirmat-ion Received
5.	Mr.Nagarajan S Assistant Engineer,	Tamilnadu Electricity Board, Devanur -pudur, Pollachi	2015	1514469434	Design of Bidirectional Switched Boost Converter for Efficient over Flow in AC/DC Hybrid Microgrid	Synopsis submitted
6.	Ms.P. Gnanambikai, Lecturer (SG)/ECE	NPTC, Pollachi.	2016	16244697154	Design of Efficient VLSI architecture for image De-Noising	Confirmat-ion Received

COMPLETED EXTERNAL SCHOLARS

S.No	Candidate Name & Designation	Presently working Institution/ Industry	Provisional Registration Details		Title of the Research Work	Present status of the PhD (Doing coursework/ Confirmation Received / Synopsis submitted/ Thesis submitted)
			Year of Reg.	Reg.No.		
1.	Mr.Senthilvel A Assistant Executive Engineer (O&M)	Tamilnadu Electricity Board, Pollachi	2015	1514469435	Efficient Design of Solar Power Plant Using Soft Computing Techniques and FPGA	Date of completion - 28.07.2021